PATENT 0397-0439P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Hironori MATSUMOTO, et al. Conf.:

5766

Appl. No.:

10/054,823

Group:

2811

Filed:

January 25, 2002

Examiner: UNASSIGNED

INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE, PROCESS OF MANUFACTURING THE SAME, IC MODULE AND IC CARD

INFORMATION DISCLOSURE STATEMENT (SUBMISSION AFTER FILING OF AN APPLICATION BUT BEFORE FINAL REJECTION OR NOTICE OF ALLOWANCE OR CONCURRENTLY WITH A RULE 53 (d) CPA APPLICATION OR WITH A RULE 1.114 RCE APPLICATION)

Assistant Commissioner for Patents Washington, DC 20231

July 2, 2002

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications, or other information submitted for consideration by the Office are listed on the PTO-1449(s), attached hereto.

COPIES (check at least one box) II.

- Submitted herewith is a legible copy of (i) each \boxtimes a. U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.
- b. Some or all of the documents listed on the PTO-1449 are not enclosed because they were cited in the International Search Report and copies should already be in the PTO file. If copies are needed, please contact the undersigned.

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III. CONCISE EXPLANATION OF THE RELEVANCE (check at least one box)

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a. DOCUMENTS IN THE ENGLISH LANGUAGE

The attached patents, publications, or other information in the English language do not require a statement of relevancy.

b. DOCUMENTS NOT IN THE ENGLISH LANGUAGE

A concise explanation of the relevance of all patents, publications, or other information listed that is not in the English language is as follows:

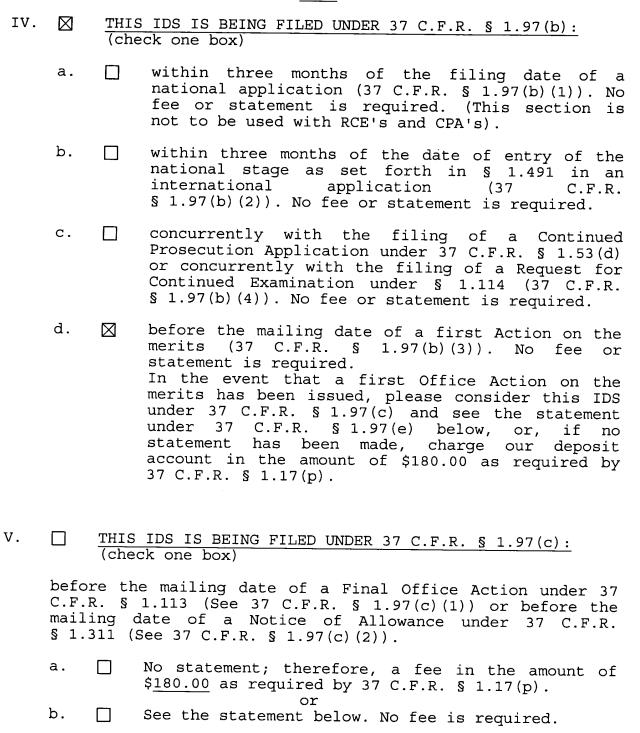
C. ENGLISH LANGUAGE SEARCH REPORT

An English language version of the search report or action that indicates the degree of relevance found by the foreign office is attached, thereby satisfying the requirement for a concise explanation. See MPEP 609(A)(3).

d. OTHER

The following additional information is provided for the Examiner's consideration.

FEES



VI.	STAT	TEMENT	UNDER 37 C.F.R. § 1.97(e) (check only one box)				
	The	unders	undersigned hereby states that				
	a.		each item of information contained in the IDS was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS; or				
	b.		no item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of IDS was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the IDS.				
	C.		Some of the items of information were cited in a communication from a foreign Patent Office. As to this information, the undersigned states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby states that no item of this remaining information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application and, to the best of my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement.				
VII.	PAYM	ENT OF	FEES (check one box)				
		A che C.F.R. fee.	ck in the amount of $$180.00$ as required by 37. $$1.17(p)$ is enclosed for the above-identified				
		amount	c charge Deposit Account No. 02-2448 in the required by 37 C.F.R. § 1.17(p) for the above- ated fee. A triplicate copy of this paper is ned.				
	\boxtimes	No fee	e is required.				

If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 02-2448.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

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0397-0439P Enclosures:

CG/cb

□ PTO-1449

□ Documents (15)

Fee

□ Other:

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Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

ATTY DOCKE NO.

APPLICATION NO. 10/054,823

APPLICANT

Hironori MATSUMOTO, et al.

FILING DATE

January 25, 2002

GROUP 2811

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EXAMINER PULLAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	5,323,053	1994-06-21	Luryi, et al.			
	3,964,084	1976-06-15	Andrews, Jr., et al			
	4,662,058	1987-05-05	Cirillo, Jr. et al			
	5,956,137	1999-09-21	Lim, et al.	_	•	
	6,122,494	2000-09-19	Tuttle			
	5,731,691	1998-03-24	Noto			
ARA	5,225,359	1993-07-06	DeLong		_	

FOREIGN PATENT DOCUMENTS

INAI	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
						YES	NO
	WO 00/74126 A1	2000-12-07	PCT				
Carrier Contract of the Contra	EP 0 702 316 A1	1996-03-20	EUROPE				
	EP 0 831 415 A2	1998-03-25	EUROPE				
	EP 0 902 476 A1	1999-03-17	EUROPE				
	EP 0 580 242 A1	1994-01-26	EUROPE				
					,,		

OTHER DOCUMENTS (Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.

"A New BiCMOS Gate Array Cell with Diode Connected Bipolar Driver" T. HORIUCHI, et al, IEEE 1990 Bipolar Circuits and Technology Mtg. pp. 124-127

"Metal Silicide Schottky Barrier Diodes", C. M. HSIEH IBM Technical Disclosure Bulletin Vol. 21. No. 3 August 1978, pg. 1026

"A 0.4-Micron Fully Complementary BiCMOS Technology for Advanced Logic and Microprocessor Applications" Shih Wei Sun, et al., IEDM Dec. 8-11, 1991, pp. 91-85-88

EXAMINER

DATE CONSIDERED

10/30/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.